

Docket No.: M4065.0100/P100-B
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Howard E. Rhodes

Application No.: Not Yet Assigned

Group Art Unit: N/A

Filed: Concurrently Herewith

Examiner: Not Yet Assigned

For: CMOS IMAGER WITH SELECTIVELY
SILICIDED GATES

INFORMATION DISCLOSURE STATEMENT (IDS)

MS Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 CFR 1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement accompanies the new patent application submitted herewith.

A copy of each reference on PTO/SB/08 is attached.

Application No.: Not Yet Assigned

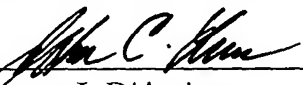
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The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 04-1073, under Order No. M4065.0100/P100-B. A duplicate copy of this paper is enclosed.

Dated: July 14, 2003

Respectfully submitted,

By


Thomas J. D'Amico

Registration No.: 28,371

John C. Luce

Registration No.: 34,378

DICKSTEIN SHAPIRO MORIN &
OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorneys for Applicant

Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>			Complete if Known		
			Application Number	Not Yet Assigned	
			Filing Date	Concurrently Herewith	
			First Named Inventor	Howard E. Rhodes	
			Art Unit	N/A	
			Examiner Name	Not Yet Assigned	
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¹Applicant's unique citation designation number (optional). ²Applicant is to place a check mark here if English language Translation is attached.